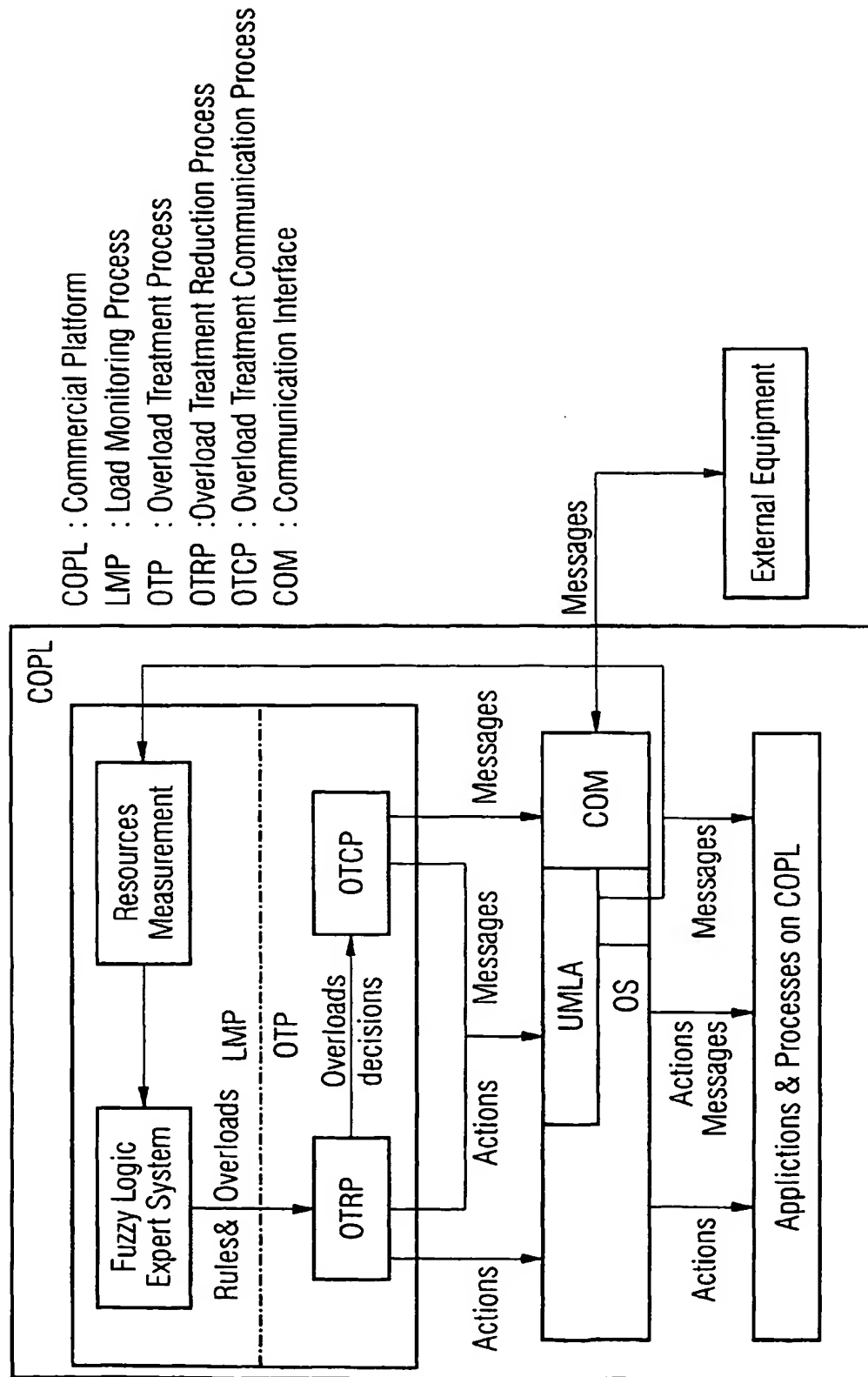
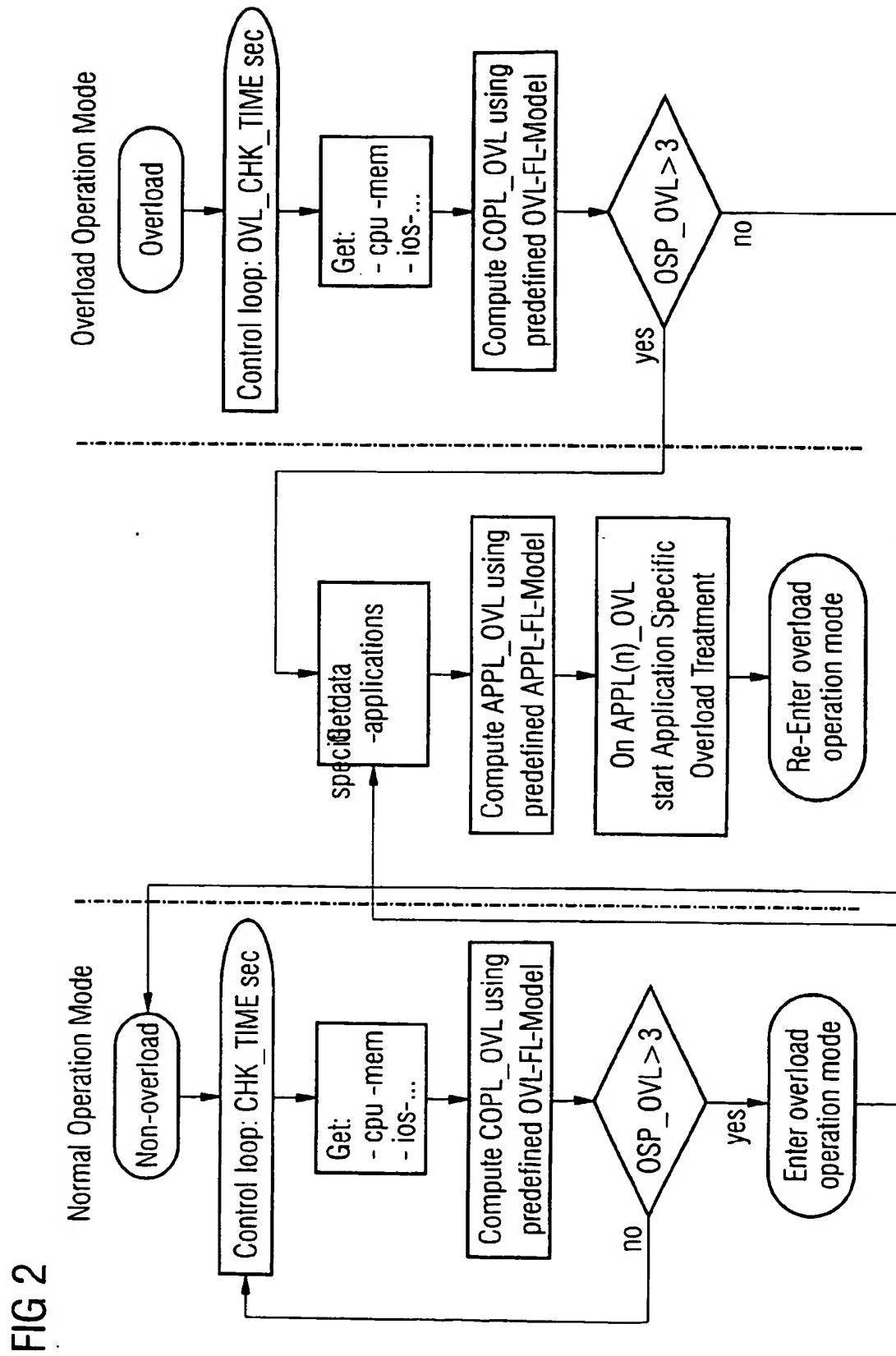


FIG 1

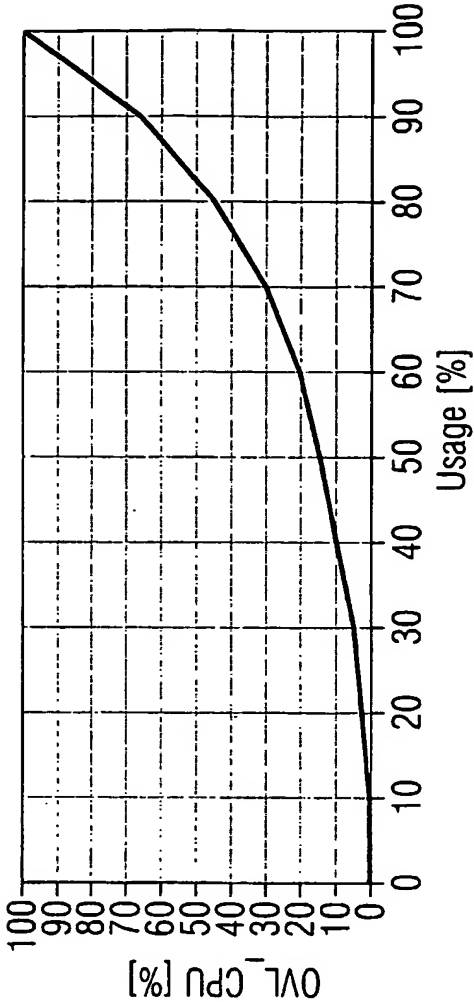




CPU

FIG 3

Usage	OVL_CPU
0 %	0 %
10 %	0 %
20 %	3 %
30 %	5 %
40 %	10 %
50 %	15 %
60 %	20 %
70 %	30 %
80 %	45 %
90 %	65 %
100 %	100 %



MEMORY

FIG 4

Usage	OVL_MEM
0 %	0 %
10 %	3 %
20 %	5 %
30 %	10 %
40 %	25 %
50 %	50 %
60 %	75 %
70 %	90 %
80 %	95 %
90 %	100 %
100 %	100 %

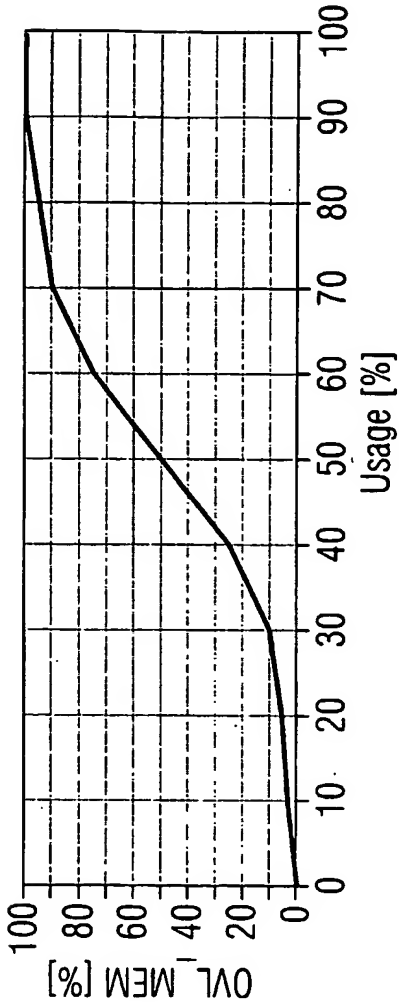


FIG 5

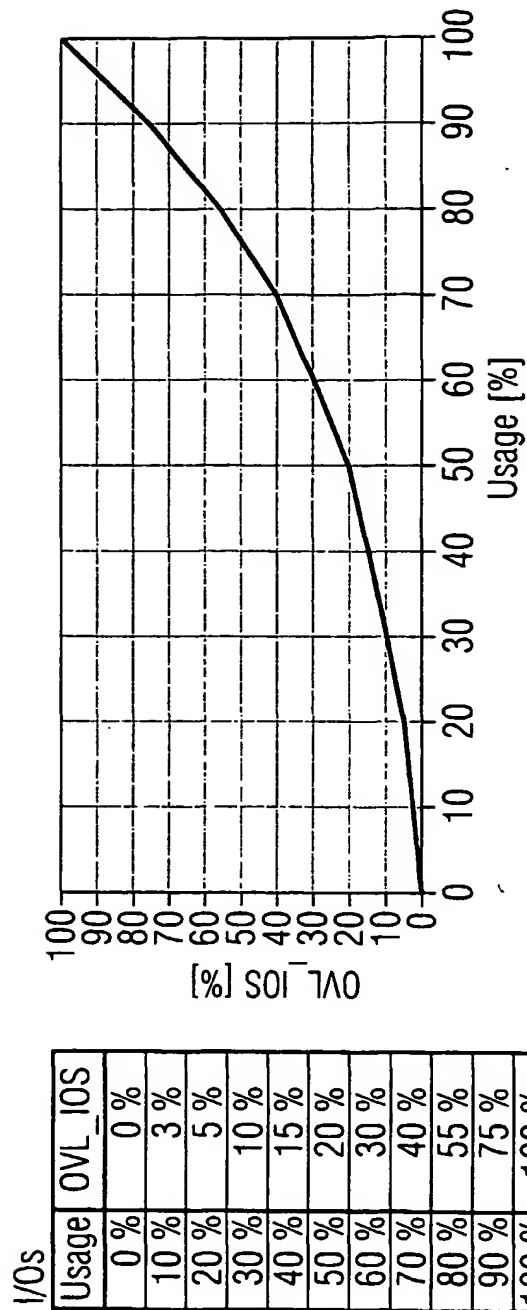


FIG 6

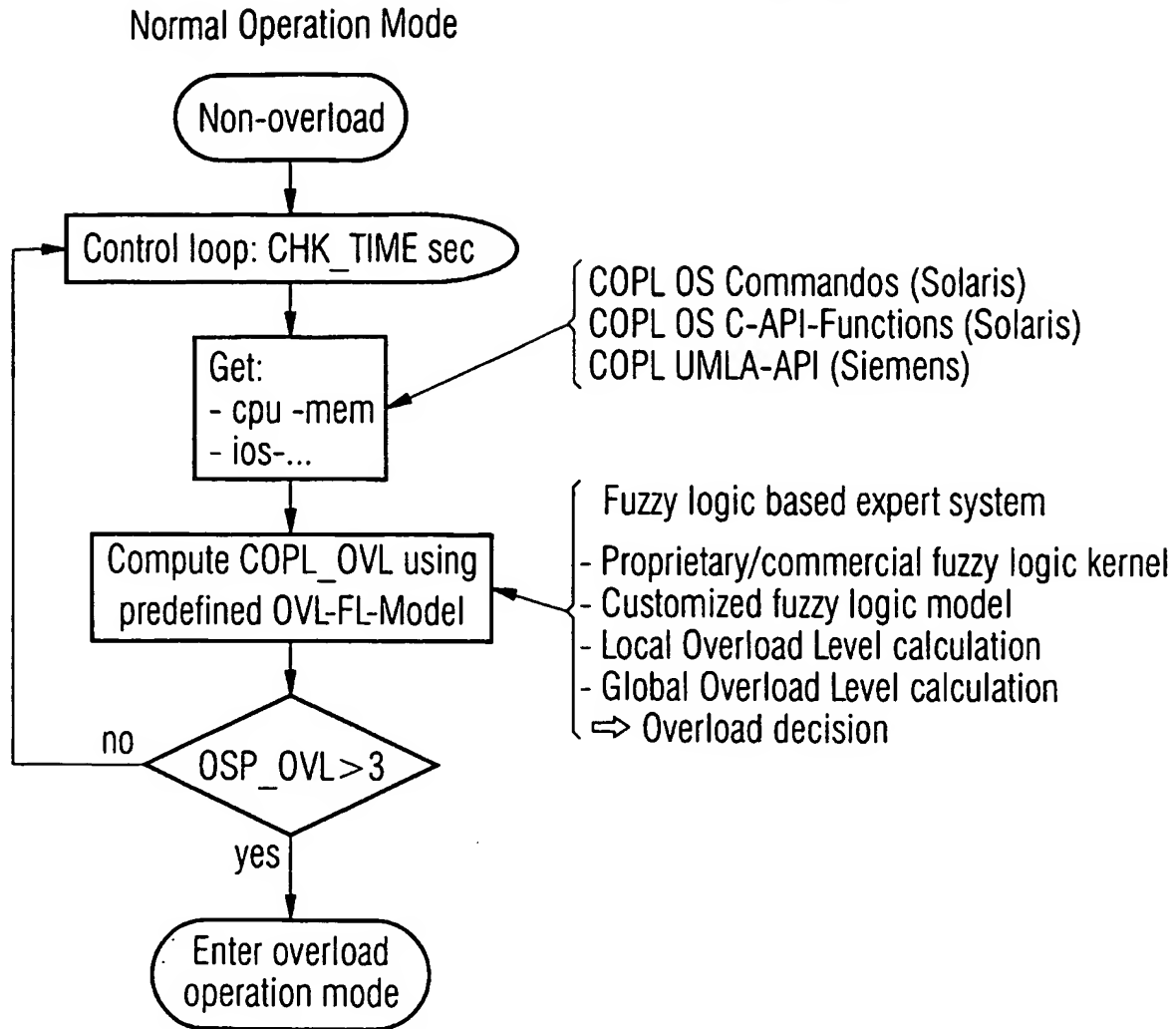


FIG 7

IF	CPU_LOAD_VERY_HIGH	AND	MEMORY_LOAD_VERY_HIGH
		AND	IOS_LOAD_VERY_HIGH
THEN	OVERLOAD_LEVEL_VERY_HIGH		
WITH	HIGHEST PROBABILITY		

FIG 8

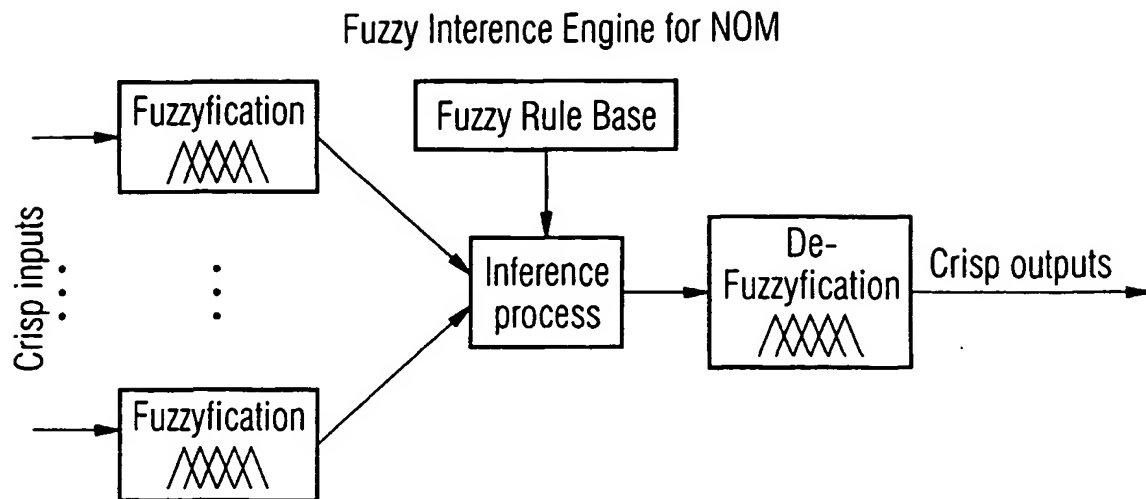


FIG 9

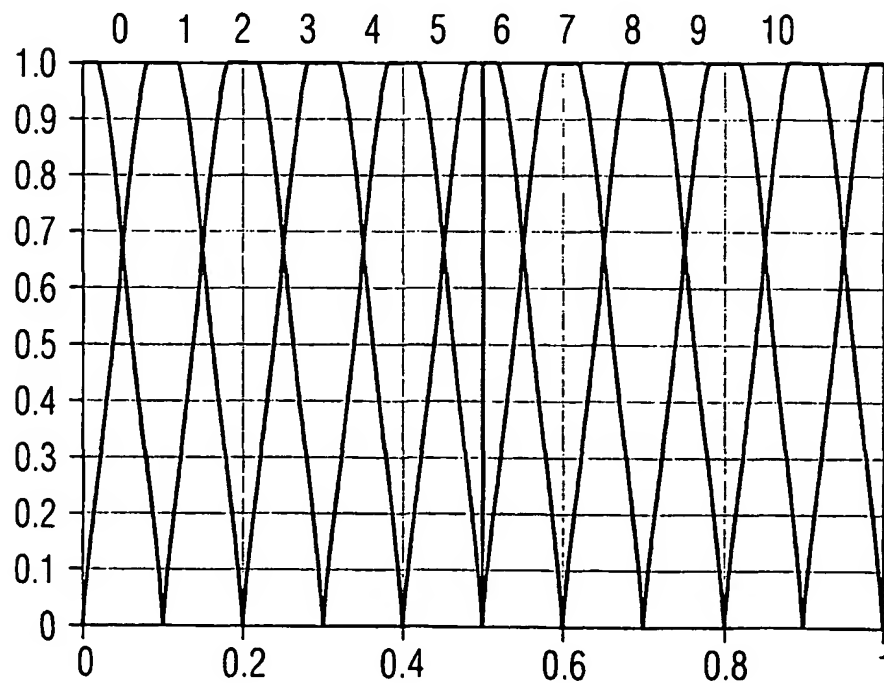


FIG 10

[SETS]

```

CPU_0 { XSTART=0 XSTOP=1 XLEN=101 FUNCT=TRAPEZE (0.000, 0.000, 0.025, 0.100, 0) } }
CPU_1 { XSTART=0 XSTOP=1 XLEN=101 FUNCT=TRAPEZE (0.000, 0.075, 0.125, 0.200, 0) } }
CPU_2 { XSTART=0 XSTOP=1 XLEN=101 FUNCT=TRAPEZE (0.100, 0.175, 0.225, 0.300, 0) } }
CPU_3 { XSTART=0 XSTOP=1 XLEN=101 FUNCT=TRAPEZE (0.200, 0.275, 0.325, 0.400, 0) } }
CPU_4 { XSTART=0 XSTOP=1 XLEN=101 FUNCT=TRAPEZE (0.300, 0.375, 0.425, 0.500, 0) } }
CPU_5 { XSTART=0 XSTOP=1 XLEN=101 FUNCT=TRAPEZE (0.400, 0.475, 0.525, 0.600, 0) } }
CPU_6 { XSTART=0 XSTOP=1 XLEN=101 FUNCT=TRAPEZE (0.500, 0.575, 0.625, 0.700, 0) } }
CPU_7 { XSTART=0 XSTOP=1 XLEN=101 FUNCT=TRAPEZE (0.600, 0.675, 0.725, 0.800, 0) } }
CPU_8 { XSTART=0 XSTOP=1 XLEN=101 FUNCT=TRAPEZE (0.700, 0.775, 0.825, 0.900, 0) } }
CPU_9 { XSTART=0 XSTOP=1 XLEN=101 FUNCT=TRAPEZE (0.800, 0.875, 0.925, 1.000, 0) } }
CPU_10 { XSTART=0 XSTOP=1 XLEN=101 FUNCT=TRAPEZE (0.900, 0.975, 1.000, 1.000, 0) } }

```

[VARIABLES]

```

1 CPU {0,1,2,3,4,5,6,7,8,9,10} IN

```

FIG 11

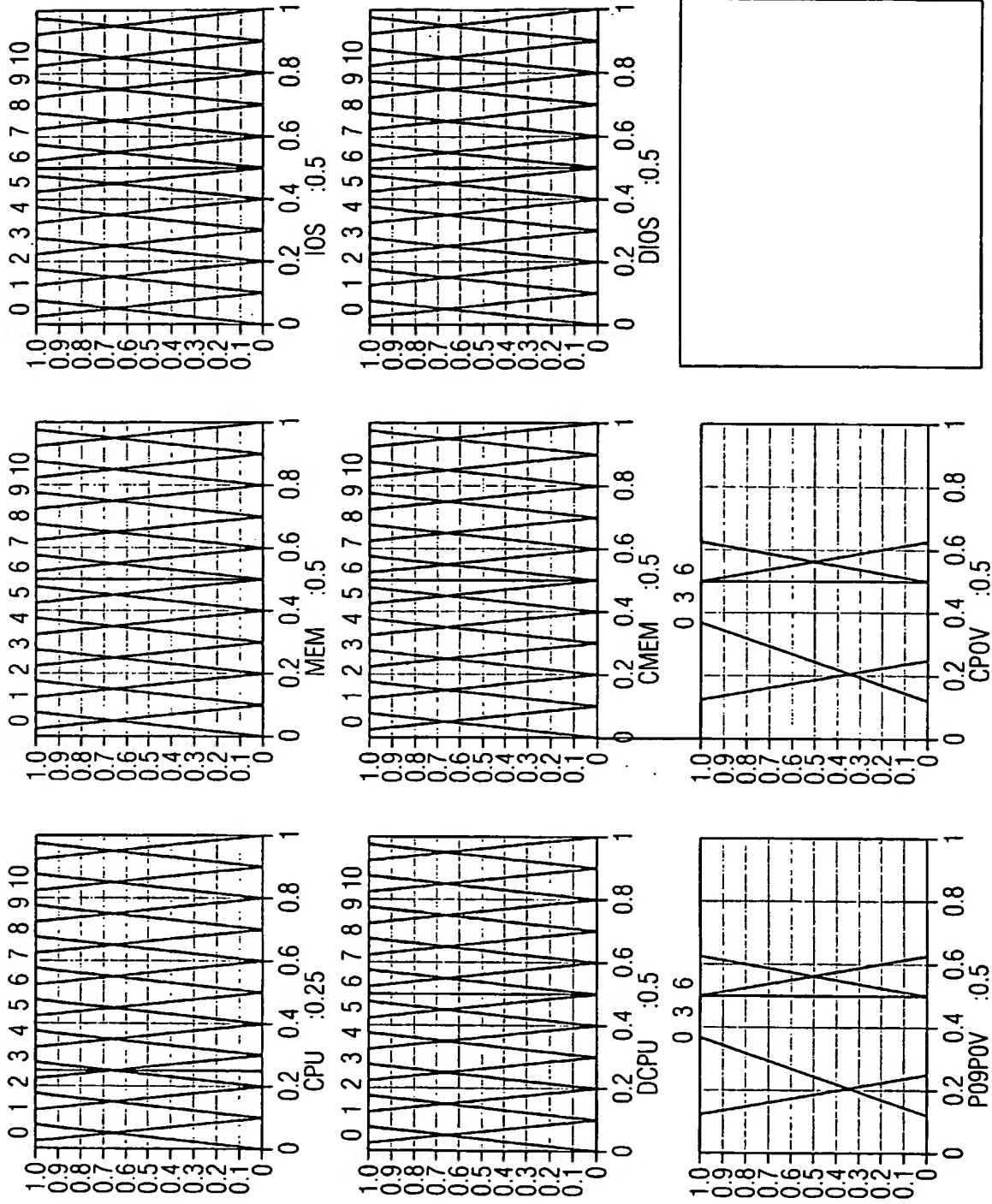


FIG 12

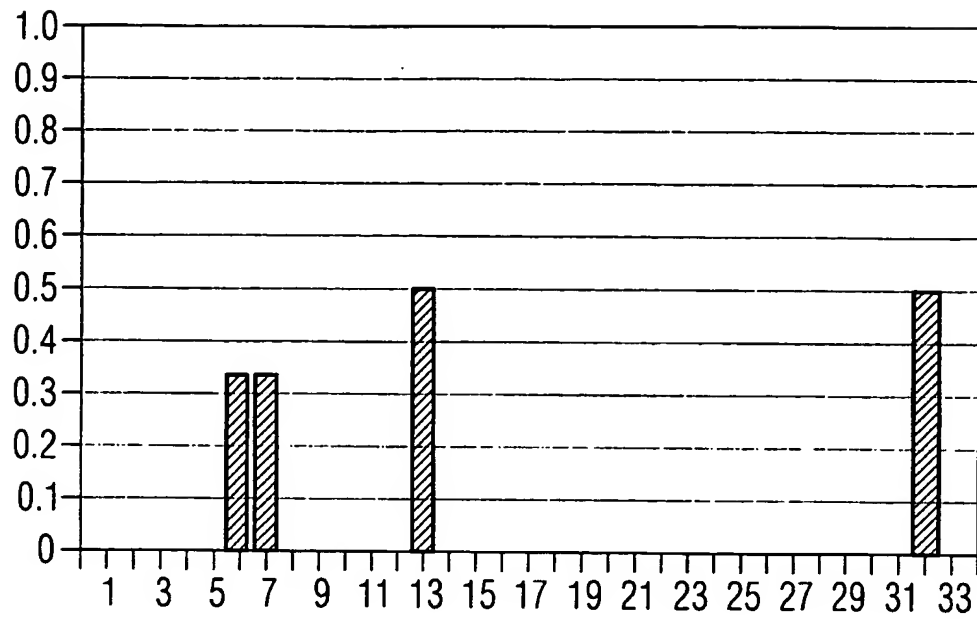


FIG 13

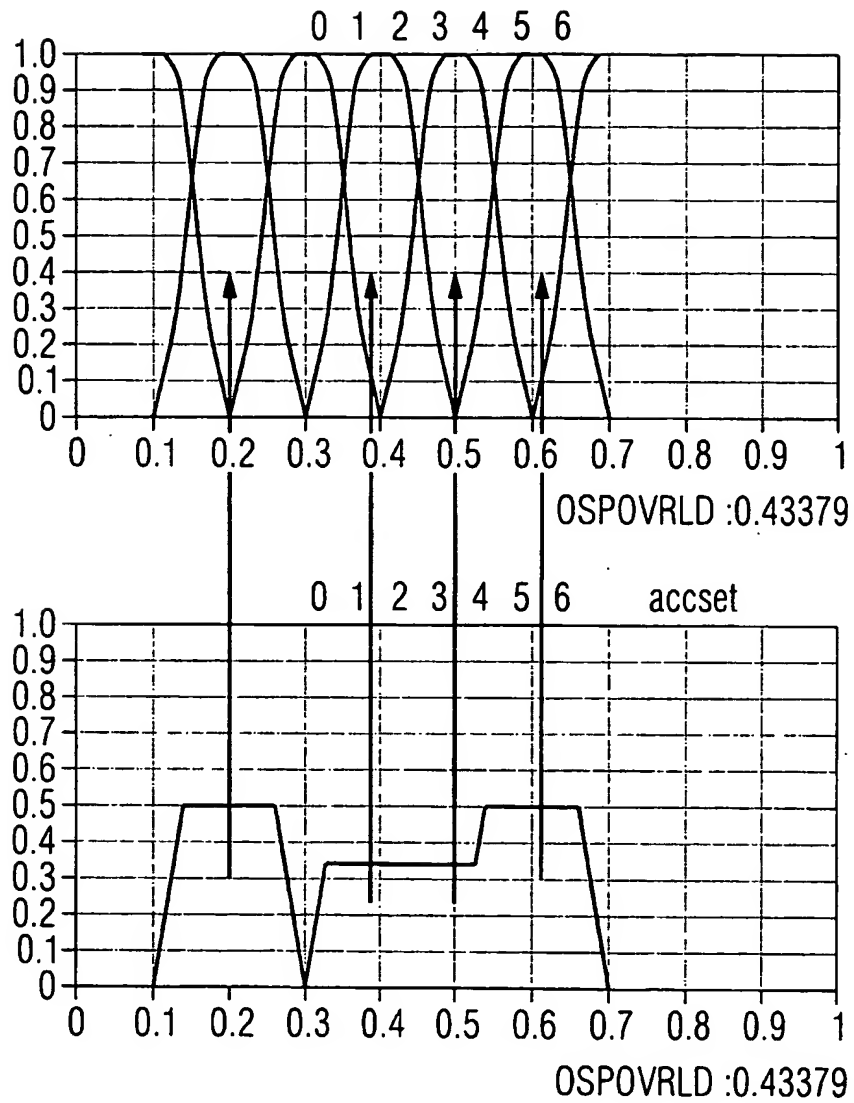


FIG 14

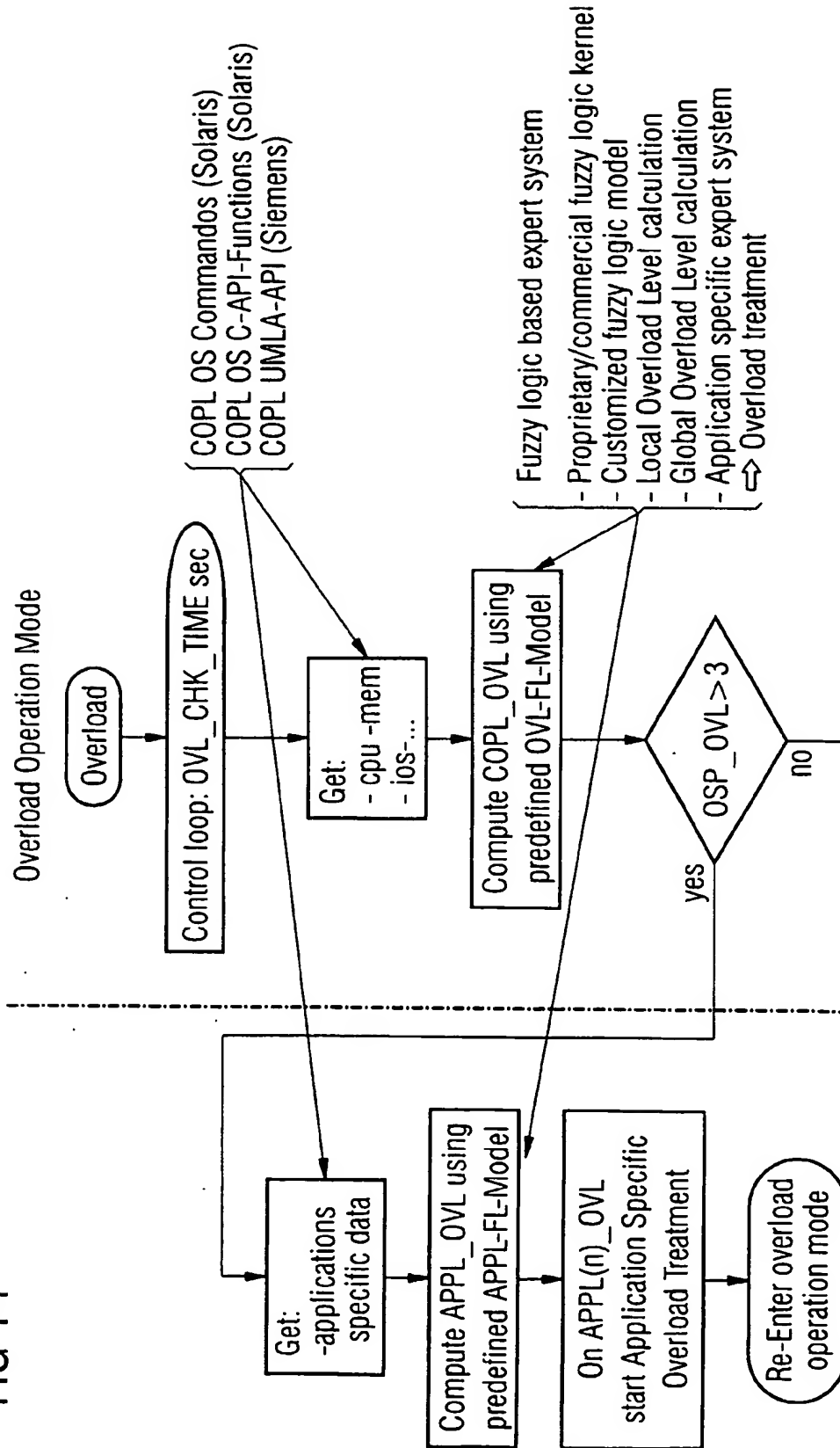


FIG 15

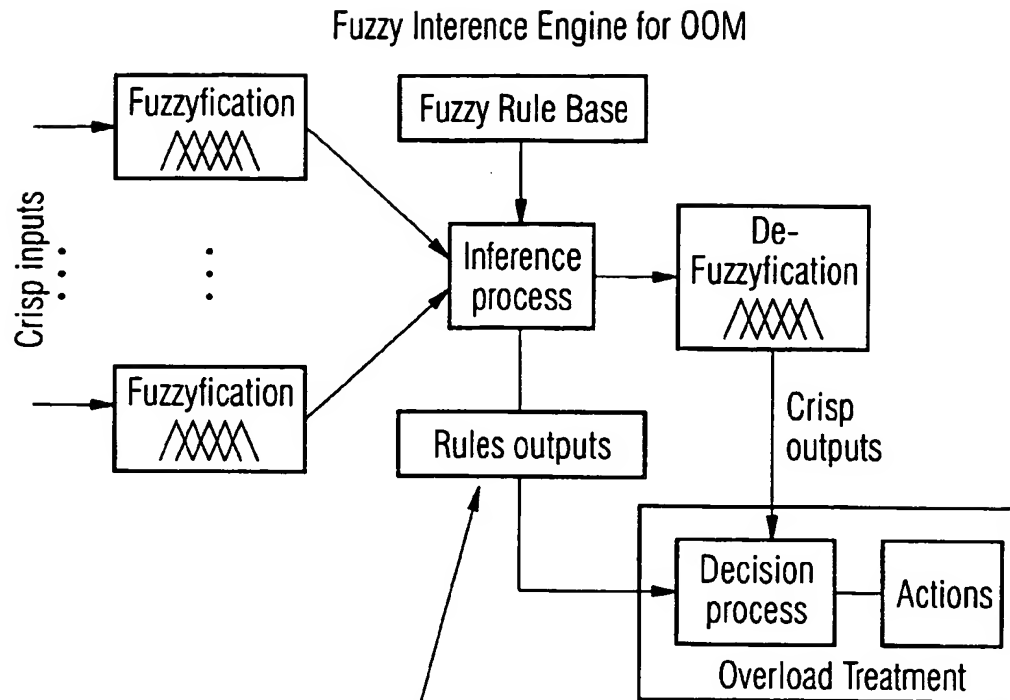
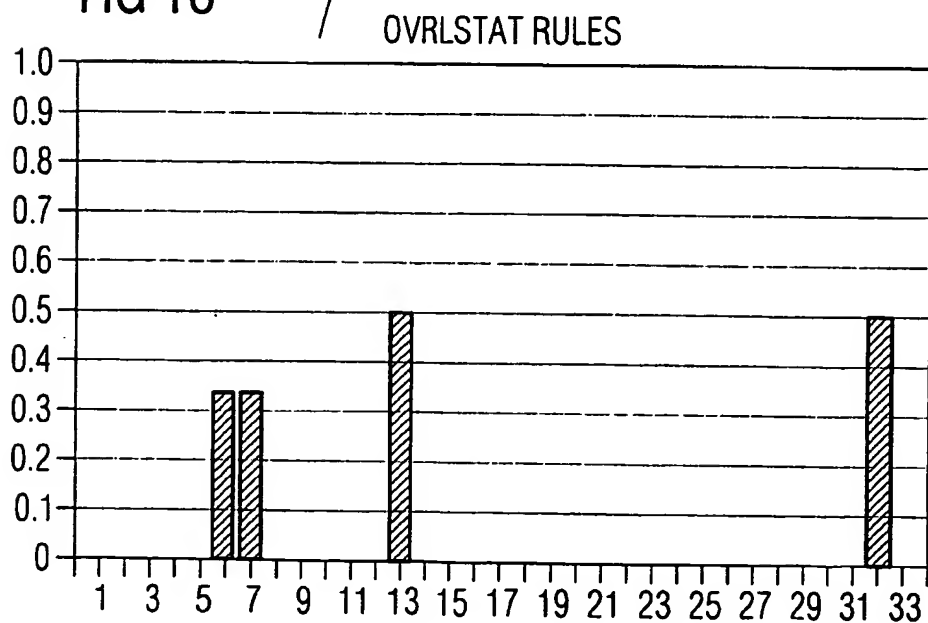


FIG 16



**ANY REFERENCE TO
FIGURE 17
SHALL BE CONSIDERED NON-EXISTENT**